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## Reduced instruction set computing - Wikipedia

<https://en.wikipedia.org/wiki/RISC>

**Reduced instruction set computing**, or **RISC** (pronounced 'risk', /É¹É²sk/), is a CPU design strategy based on the insight that a simplified instruction set provides ...

## List of instruction sets - Wikipedia

[https://en.wikipedia.org/wiki/List\\_of\\_instruction\\_sets](https://en.wikipedia.org/wiki/List_of_instruction_sets)

A list of computer central processor instruction sets: (Companies that created only a few different processors are listed at the end under "Other")

## Book Recommendations - Embedded Systems Academy

[www.esacademy.com/en/library/book-recommendations.html](http://www.esacademy.com/en/library/book-recommendations.html)

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