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## [::NIST::Syllabus::](#)

[www.nist.edu/Academics/BTech\\_CivilEngineering.html](http://www.nist.edu/Academics/BTech_CivilEngineering.html)

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## [FPGA & Verilog Design â€œ Mohammad S. Sadri - Googoolia](#)

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This page contains the complete set of materials for my **FPGA & Verilog design** course which I taught in Isfahan University of Technology, 2010.

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